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vector and tensor analysis by nawazish ali shah pdf free 10721. Field of the Invention The present invention relates to an image display device. 2. Description of the Related Art In an image display device, signal processing is generally performed in a driver IC. More specifically, in a driver IC for a light-emitting display device, in order to perform a control operation of the light-emitting display device, a test signal is input to the driver IC through a signal input line. Subsequently, a signal indicating an operation result of the light-emitting display device is output from the driver IC. Thus, a conductive state of a signal input line and a conductive state of a scan line are controlled based on a result of the test signal, and a control operation of the light-emitting display device is performed. In such a driver IC, it is necessary to test a signal input line and a scan line. More specifically, a control signal is applied from outside to a signal input line, and then, a result of the control operation is confirmed using an output signal from a scan line. However, at this time, a specific signal needs to be applied to a scan line, and a scan line is generally set in a floating state. As a result, the output signal from a scan line may be output not only during the test operation but also when the light-emitting display device is normally controlled. In this case, when the output signal from the scan line is superimposed on a test result of the output signal from the signal input line, there is a possibility that the test of the output signal from the scan line is erroneously performed, and further, in a peripheral circuit including the driver IC, it is difficult to secure a sufficiently high test accuracy. In the related art, a gate is formed between a signal input line and a scan line, and a wiring is formed between the gate and a driver IC (JP-A-2012-154191). However, in JP-A-2012-154191, a level of the signal input line and a level of the scan line are each set in a floating state, and a wiring is formed therebetween. Thus, at the wiring, an impedance mismatching may occur, and thus, it is difficult to sufficiently secure the test accuracy of the output signal from the scan line. In addition, in JP-A-2012-154191, since a path to the driver IC is formed c6a93da74d

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